

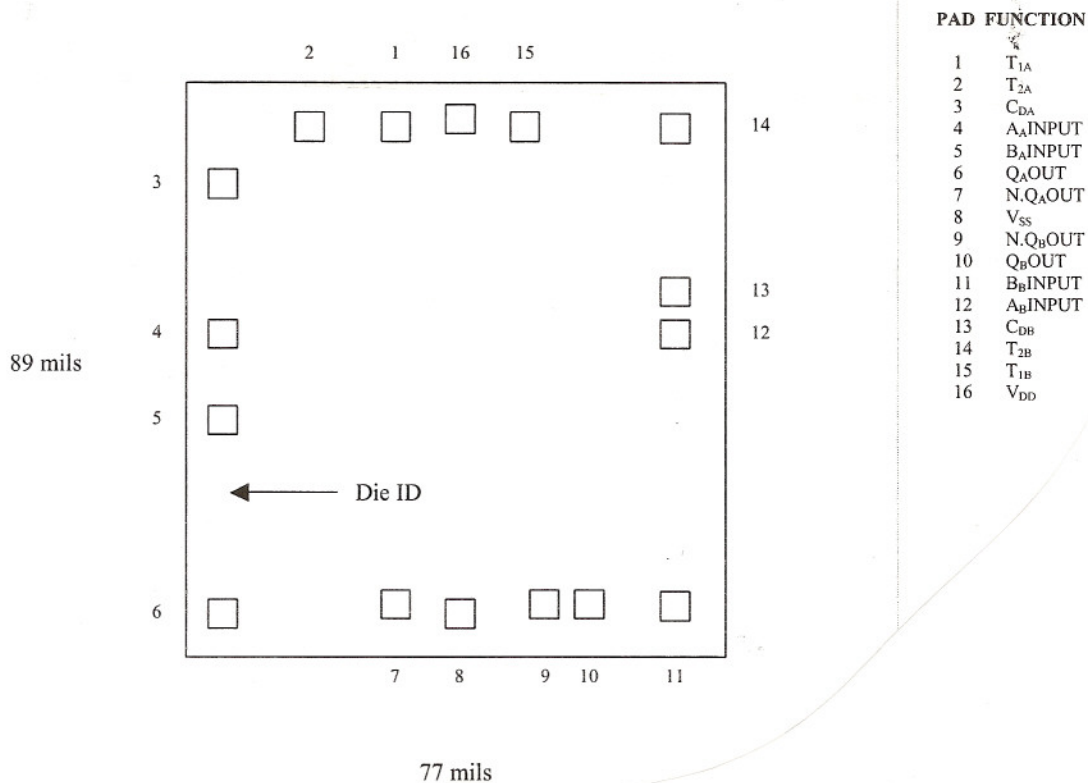


Sierra Components, Inc.

2222 Park Place Building 3 Suite E • Minden, Nevada 89423

Phone: 775.783.4940 Fax: 775.783.4947

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



Topside Metal: Al

Backside: Si

Backside Potential: Vdd

Mask Ref: CD4538BM

Bond Pads (Mils): 4 x 4

APPROVED BY:

MFG: Fairchild

DIE SIZE (Mils): 77 x 89

THICKNESS: 15

DATE: 11/27/01

P/N: CD4538BH